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APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO	. CONFIRMATION NO.		
10/698,170	70 10/30/2003		Sei-Hyung Ryu	5308-279	2502	
20792	7590	05/10/2006		EXAMINER		
MYERS BIO		BLEY & SAJOVI	TRAN, LONG K			
RALEIGH,		27	ART UNIT	PAPER NUMBER		

2818
DATE MAILED: 05/10/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	<del></del> ⊱		
	Office Action Summary	10/698,170 Examiner	RYU, SEI-HYUNG			
	<b>,</b>		Art Unit			
	- The MAILING DATE of this communication	Long K. Tran	2818			
Period fo		appears on the cover sneet	with the correspondence address			
WHIC - Exten after: - If NO - Failur Any r	DRTENED STATUTORY PERIOD FOR REI HEVER IS LONGER, FROM THE MAILING sions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. period for reply is specified above, the maximum statutory per e to reply within the set or extended period for reply will, by stately received by the Office later than three months after the mad patent term adjustment. See 37 CFR 1.704(b).	B DATE OF THIS COMMU R 1.136(a). In no event, however, may iod will apply and will expire SIX (6) Natute, cause the application to become	NICATION.  of a reply be timely filed  IONTHS from the mailing date of this communication  ABANDONED (35 U.S.C. § 133).			
Status						
2a) <u>□</u> 3) <u>□</u>	Responsive to communication(s) filed on <u>28</u> This action is <b>FINAL</b> . 2b) To Since this application is in condition for allow closed in accordance with the practice under	his action is non-final. wance except for formal m	• •	is		
Dispositi	on of Claims					
5) □ 6) ⋈ 7) ⋈ 8) □ Applicati 9) □ 10) □	Claim(s) 1-30 is/are pending in the application 4a) Of the above claim(s) is/are without Claim(s) is/are allowed.  Claim(s) 1-10,12-20 and 22-30 is/are reject Claim(s) 2,11 and 21 is/are objected to.  Claim(s) are subject to restriction and con Papers  The specification is objected to by the Example drawing(s) filed on is/are: a) and applicant may not request that any objection to the Replacement drawing sheet(s) including the contraction of the oath or declaration is objected to by the	drawn from consideration.  ed.  d/or election requirement.  siner.  accepted or b) objected the drawing(s) be held in aberrection is required if the drawington.	yance. See 37 CFR 1.85(a). ng(s) is objected to. See 37 CFR 1.121			
Priority u	nder 35 U.S.C. § 119					
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
2) Notice 3) Inform	e of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/ No(s)/Mail Date 10/30/03,2/20/04.	Paper N	w Summary (PTO-413) lo(s)/Mail Date of Informal Patent Application (PTO-152) 			

IDS: 8/13/04, 3/17/05, 4/29/05

Art Unit: 2818

#### **DETAILED ACTION**

### Election/Restrictions

1. The Restriction requirement in the previous Office Action on January 30, 2006 has been withdrawn by the examiner.

# Claim Objections

2. Claim **2** is objected to because of the following informalities: line 2: "the first ptype" should be --the n-type--. Typo error. Appropriate correction is required.

## Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

- 4. Claims 1 4, 6, 12, 13, 15, 16, 18 20, 22, 23 and 27 29 are rejected under 35 U.S.C. 102(e) as being anticipated by Saitoh et al. (US Patent No. 6,700,156).
- 5. Regarding claims **1, 27, 28** and **29**, Saitoh, in figures 1, 40B 41E, discloses a silicon carbide metal-oxide semiconductor field effect (MOSFET) transistor unit cell, comprising:

an n-type silicon carbide drift layer (11; column 28, lines 39 – 46);

a first p-type silicon carbide well regions (12) adjacent the drift layer (11) and spaced apart;

Art Unit: 2818

a first n-type silicon carbide region (13) within the first p-type silicon carbide region (12);

an oxide layer (23) on the drift layer (11), the first p-type silicon carbide region (12), and the first n-type silicon carbide region (13); and

a first n-type silicon carbide limiting region comprises a first portion (11a), disposed between the drift layer (11) and a portion of the first p-type silicon carbide (12), and a second portion (11b) adjacent spaced apart well regions (12) and the drift layer (11), wherein the n-type limiting region (11a) and between wells has a carrier concentration (n) that is greater than a carrier concentration (n<sup>-</sup>) of the drift layer (11).

Regarding claim **2**, Saitoh discloses the portion of the first n-type silicon carbide region (11a) is adjacent a floor of the first p-type silicon carbide region (12).

Regarding claim 3, Saitoh discloses the n-type limiting region (11b; column 21, lines 66 – 67) is disposed adjacent a sidewall of the first p-type silicon carbide region (12).

Regarding claim 4, Saitoh discloses the n-type limiting region comprises the first portion (11a) and the second portion ((11b); adjacent to the sidewall of the first p-type silicon carbide (12)) and wherein the first portion (11a) has a carrier concentration greater than a carrier concentration of the second portion ((11b); column 21, line 66 – 67 and column 22, lines 1 - 3).

Regarding claim 6, Saitoh discloses MOSFET comprising:

a gate contact (24) on the oxide layer (23);

Source contact (22) on the first n-type silicon carbide region (13); and

Art Unit: 2818

Drain contact (21) on the drift layer (11; column 5, lines 36 – 51) opposite the oxide layer (23).

Regarding claims **12** and **19**, Saitoh discloses the n-type limiting (third) silicon carbide regions (11a) comprising implanted n-type regions in the drift layer (column 22, lines 18 – 22).

Regarding claim **15**, Saitoh discloses a silicon carbide metal-oxide semiconductor field effect transistor (MOSFET) comprising:

a drift layer of n-type silicon carbide (11);

first regions (12) of p-type silicon carbide adjacent the drift layer (11);

a first region of n-type silicon carbide (11b) disposed between peripheral edges of the first regions (12) of p-type silicon carbide;

second regions of n<sup>+</sup>-type silicon carbide (13) within the first regions of p-type silicon carbide (12), wherein the second regions of n<sup>+</sup>-type silicon carbide have a carrier concentration greater than a carrier concentration (n<sup>-</sup>) of the drift layer and are spaced apart from the peripheral edges of the first regions of p-type silicon carbide (12);

an oxide layer (13) on the drift layer, the first region of n-type silicon carbide (11b) and the second regions of n-type silicon carbide (13);

third regions of n-type silicon carbide (11a) disposed beneath the first regions of p-type silicon carbide (12) and between the first regions of p-type silicon carbide (12) and the drift layer (11), wherein the third regions of n-type silicon carbide (11a) have a carrier concentration (n) greater than the carrier concentration (n') of the drift layer (11);

Art Unit: 2818

source contacts (22) on portions of the second regions (13) of n-type silicon carbide;

a gate contact (24) on the oxide layer (23); and

Drain contact (21) on the drift layer (11; column 5, lines 36 – 51) opposite the oxide layer (23).

Regarding claim **16**, Saitoh discloses the third regions of n-type silicon carbide are adjacent the peripheral edges of the first regions of p-type silicon carbide (12).

Regarding claim **18**, Saitoh discloses the first region of n-type silicon carbide (11b) comprise a region of the drift layer (11).

Regarding claim **20**, Saitoh discloses the first region of n-type silicon carbide (11b) has a higher carrier concentration (n) than a carrier concentration (n) of the drift layer and has a lower carrier concentration than a carrier concentration (n) of the third regions of n-type silicon carbide (11a). see column 21, line 66 – 67 and column 22, lines 1-3.

Regarding claims **13** and **22**, Saitoh discloses an n-type silicon carbide layer (15) between the drift layer (11) and the drain contact (21), wherein the n-type silicon carbide layer has a higher carrier concentration (n<sup>+</sup>) than the carrier concentration (n-) of the drift layer (11).

Regarding claim **23**, Saitoh discloses the n-type silicon carbide layer comprises an n-type silicon carbide substrate (column 5, lines 52 – 57and column 28, lines 44 – 48).

Application/Control Number: 10/698,170 Page 6

Art Unit: 2818

# Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claims **9**, **10**, **25** and **26** are rejected under 35 U.S.C. 103(a) as being unpatentable over Saitoh et al. (US Patent No. 6,700,156).
- 8. Regarding claims **9, 25** and **26**, Saitoh discloses all limitations of claim 1 or 15 but fails to teach the limiting (third) regions of n-type silicon carbide have a thickness of from about 0.5 to about 1.5 micrometers and a carrier concentration of from 1 x  $10^{15}$  to about 5 x  $10^{17}$  cm  $^{-3}$ .

Art Unit: 2818

However, it would have been well known in the art that the selection of those parameters such as energy, concentration, temperature, time, molar fraction, depth, thickness, etc., would have been obvious and involve routine optimization which has been held to be within the level of ordinary skill in the art. "Normally, it is to be expected that a change in energy, concentration, temperature, time, molar fraction, depth, thickness, etc., or in combination of the parameters would be an unpatentable modification. Under some circumstances, however, changes such as these may impart patentability to a process if the particular ranges claimed produce a new and unexpected result which is different in kind and not merely degree from the results of the prior art ... such ranges are termed "critical ranges and the applicant has the burden of proving such criticality.... More particularly, where the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation.

Regarding claim **10**, Saitoh discloses the claimed invention of claims 1 and 6 except for the gate contact comprising polysilicon or metal.

However, polysilicon and metal are well known material in the art used for gate electrode and gate contact. Reference to Alok et al. (column 5, lines 49 – 51) is cited merely for purpose of showing an example, but not used in the rejection.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to provide Saitoh MOSFET with well known polysilicon or metal as a gate contact, since it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use.

Saitoh et al. (US Patent No. 6,700,156).

Art Unit: 2818

9. Claims 1, 5, 6, 10, 14,15 and 24 are rejected under 35 U.S.C. 103(a) as being obvious over Ryu et al. (US Patent Application Publication No. 2002/0038891 in view of

Page 8

10. Regarding to claims **1** and **15**, Ryu, in figure 1B, discloses a silicon carbide metal-oxide semiconductor field effect transistor (MOSFET) comprising:

a drift layer of n-type silicon carbide (N drift layer);

first regions of p-type silicon carbide adjacent the drift layer;

a first region of n-type silicon carbide disposed between peripheral edges of the first regions of p-type silicon carbide;

second regions of n<sup>+</sup>-type silicon carbide (N<sup>+</sup>) within the first regions of p-type silicon carbide, wherein the second regions of N<sup>+</sup>-type silicon carbide have a carrier concentration greater than a carrier concentration (N<sup>-</sup>) of the drift layer and are spaced apart from the peripheral edges of the first regions of p-type silicon carbide;

an oxide layer on the drift layer, the first region of n-type silicon carbide and the second regions of n-type silicon carbide;

source contacts on portions of the second regions of n-type silicon carbide;

a gate contact on the oxide layer; and

drain contact on the drift layer opposite the oxide layer.

Ryu fails to teach third regions of n-type silicon carbide disposed beneath the first regions of p-type silicon carbide and between the first regions of p-type silicon carbide and the drift layer, wherein the third regions of n-type silicon carbide have a carrier concentration greater than the carrier concentration of the drift layer.

Art Unit: 2818

However, Saitoh discloses a similar MOSFET to the claimed invention including third regions of n-type silicon carbide (11a) disposed beneath the first regions of p-type silicon carbide (12) and between the first regions of p-type silicon carbide (12) and the drift layer (11), wherein the third regions of n-type silicon carbide (11a) have a carrier concentration (n) greater than the carrier concentration (n) of the drift layer (11) to prevent resistance from expanding to the broad n—type drift layer (11) from JFET region interposed between the p-type base layer (12). See column 7, lines 29 – 38.

It would have been obvious to one of ordinary skill in the art at the time of the invention was made to provide the MOSFET of Ryu with the n-type limiting region (11a) of Saitoh, in order to reduce the resistance between adjacent p-type base layer.

Regarding claim **5**, the combined Ryu and Saitoh device includes the first p-type silicon carbide region is implanted with aluminum (Ryu: paragraph [0017]).

Regarding claim **6**, the combined Ryu and Saitoh device, particularly Saitoh in figures 1, 40B – 41E, comprising:

a gate contact (24) on the oxide layer (23);

Source contact (22) on the first n-type silicon carbide region (13); and

Drain contact (21) on the drift layer (11; column 5, lines 36 – 51) opposite the oxide layer (23).

Regarding claims **7**, **8**, **17** and **30**, the combined Ryu and Saitoh device, particularly Saitoh, appears to include the n-type region comprising an epitaxial layer silicon carbide on the drift layer (11), and wherein the p-type well regions (12) are disposed in but not through the epitaxial layer.

Regarding claim **10**, the combined Ryu and Saitoh device comprising gate contact is polysilicon (Ryu, paragraph [0019]).

Regarding claims **14** and **24**, the combined Ryu and Saitoh device includes a second p-type silicon carbide region disposed within the first p-type silicon carbide region adjacent to the first n-type silicon carbide region (Ryu, figure 1B).

## Allowable Subject Matter

- 11. Claims **11** and **21** are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 12. The following is an examiner's statement of reasons for the indication of allowable subject matter: Claims 11 and 21 are allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach:

An n-type epitaxial layer (27) on the first p-type silicon carbide region (20) and the first region of n-type silicon carbide (24).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Art Unit: 2818

### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Long K. Tran whose telephone number is 571-272-1797. The examiner can normally be reached on Mon-Thu.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

**LKT** 

May 07, 2006